

L Number	Hits	Search Text	DB	Time stamp
1	48048	SRAM or (static adj5 random adj5 access adj5 memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:39
2	28	(SRAM or (static adj5 random adj5 access adj5 memory)) same (true adj3 bit\$1line)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:41
3	6	((SRAM or (static adj5 random adj5 access adj5 memory)) same (true adj3 bit\$1line)) same (continuous adj3 bit\$1line)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:41
4	69	(SRAM or (static adj5 random adj5 access adj5 memory)) and (true adj3 bit\$1line)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:41
5	6	((SRAM or (static adj5 random adj5 access adj5 memory)) and (true adj3 bit\$1line)) and (continuous adj3 bit\$1line)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:41
6	6	("true bit line" or (true adj3 bit\$1line)) same (continuous adj3 bit\$1line)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:43
7	16	("true bit line" or (true adj3 bit\$1line)) and (continuous adj3 bit\$1line)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:43
8	4	((("true bit line" or (true adj3 bit\$1line)) and (continuous adj3 bit\$1line)) and pre\$1charg\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:43
9	11	("bit line" or (true adj3 bit\$1line)) same (continuous adj3 bit\$1line)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:43
10	27	("bit line" or (true adj3 bit\$1line)) and (continuous adj3 bit\$1line)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:44
11	4	((("bit line" or (true adj3 bit\$1line)) and (continuous adj3 bit\$1line)) and pre\$1charg\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:44
12	75	bit\$1line and (complement adj3 bit\$1lin\$1) and pre\$1charg\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:45
14	68	(bit\$1line and (complement adj3 bit\$1lin\$1) and pre\$1charg\$3) and (FET or "field effect transistor" or transistor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:45

15	36	((bit\$lline and (complement adj3 bit\$llin\$1) and pre\$1charg\$3) and (FET or "field effect transistor" or transistor)) and SRAM	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:46
16	32	((bit\$lline and (complement adj3 bit\$llin\$1) and pre\$1charg\$3) and (FET or "field effect transistor" or transistor)) and SRAM) and 365/\$7.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:46
17	5	((bit\$lline and (complement adj3 bit\$llin\$1) and pre\$1charg\$3) and (FET or "field effect transistor" or transistor)) and SRAM) and 365/154.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:46
18	9	((bit\$lline and (complement adj3 bit\$llin\$1) and pre\$1charg\$3) and (FET or "field effect transistor" or transistor)) and SRAM) and 365/201.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:46
19	0	adam-chad-a.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:47
20	0	adams-chad-a.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:47
21	0	aipperspach-anthony.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:47
22	42	aipperspach-anthony-\$.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:47
23	0	christensen-todd.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:48
24	41	christensen-todd-\$.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:48
25	0	freiburger-peter.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:48
26	12	freiburger-peter-\$.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:48